

**Power Supply Apparatus,
Switching Power Supply Apparatus, And
Image Forming Apparatus**

5 BACKGROUND OF THE INVENTION

Field of the Invention

The invention relates to a power supply apparatus and an image forming apparatus, and particularly to a power supply apparatus suitable as 10 the power source of an image forming apparatus such as a copying machine or a printer using an electrophotographic process, and an image forming apparatus using the power supply apparatus.

Description of Related Art

15 Due to the rapid advance of digital technology and semiconductor integrated circuit technology in recent years, it has been practiced to digitize the control of the switching power supply of a copying machine and printer and a high voltage source, make a 20 chip for control and mount it in a power supply apparatus.

An example of the conventional art will hereinafter be schematically described with reference to Figs. 1, 2, and 3 of the accompanying drawings.

25 Description will be made here with attention paid only to a high voltage output called a high voltage A. When there are a plurality of outputs,

time division control is effected by the use of a multiplexer or the like, whereby a plurality of outputs are also realized.

Each part will first be described. An inverter
5 transformer T1 converts a voltage inputted to a primary side in conformity with the turn ratio between the primary side and a secondary side and outputs a predetermined voltage to the secondary side.

A switching element Q4 drives the inverter
10 transformer T1. A sample hold circuit 9 latches an input signal for a predetermined period. An A/D converter 10 converts an analog signal into a digital signal. A comparator 11 outputs the result of the comparison of the magnitude of two inputs. A pulse
15 width modulation (PWM) circuit 13 prepares a pulse signal (PWM signal) of an ON/OFF time ratio conforming to the value of an up-down counter 12. A controller 3 governs the control of each element.

Description will now be made of the connection
20 of each part and element and a series of operations.

(a) High Voltage Producing Means 1

A predetermined voltage as B-voltage is inputted to the primary side input of the inverter transformer T1, and the other end of the inverter transformer T1
25 is connected to the switching element Q4. A pulse signal (PWM signal) is inputted from the PWM circuit 13 in high voltage control means 2 which will be

described later to the gate of the switching element Q4. The switching element Q4 performs a switching operation in conformity with the PWM signal to thereby obtain a pulse output conforming to the turn ratio and the PWM signal on the secondary side of the inverter transformer T1. The pulse output is rectified by a diode D301 and a capacitor C201, and a DC high voltage output is produced on the cathode side of the diode D301. A boosting and rectifying circuit 1-2 and driving means 1-1 are generally called fly-back type (or ON/OFF type) converters, and output a higher voltage as the ON time percentage in the switching operation of the switching element Q4 is greater.

Resistors R101 and R102 are connected to the high voltage output, and also a voltage dropped by the resistance division of each resistor, i.e. the detection signal of the high voltage output, is outputted.

20 (b) High Voltage Control Means 2

The high voltage output detection signal obtained by the resistors R101 and R102 in the high voltage producing means 1 is inputted to the sample hold circuit 9. The sample hold circuit 9 holds the high voltage output detection signal for a predetermined period. The high voltage output detection signal of the A/D converter 10 is converted

into digital data and is inputted to one of the input terminals of the digital comparator 11.

A sequence control unit 200 for controlling a high voltage unit 100 outputs digital data 5 corresponding to the output set value (target value) of a high voltage output A. This target value is inputted to the other input terminal of the digital comparator 11.

The digital comparator 11 compares the high 10 voltage output detection signal converted into the digital data with the target value given from the sequence control unit 200. When the high voltage output detection signal is smaller than the target value, the output of the up-down counter 12 is made 15 greater by a predetermined number, and when the high voltage output detection signal is greater than the target value, the output of the up-down counter 12 is made smaller by the predetermined number. The PWM circuit 13 produces an appropriate PWM signal in 20 conformity with the value of the up-down counter 12. The PWM signal is a driving signal for the switching element Q4 in the high voltage producing means 1, and is a signal for determining the switching time 25 percentage of the switching element Q4. When the output of the up-down counter 12 becomes great, the time percentage for which the switching element Q4 is switched on is made great to thereby make the high

- voltage produced by the high voltage output producing means 1 great. Also, when the count value which is the output of the up-down counter 12 becomes small, the time percentage for which the switching element
- 5 Q4 is switched on is made small to thereby make the high voltage produced by the high voltage producing means small. By the above-described operation, the high voltage output A is controlled to a predetermined value conforming to the target value.
- 10 On the other hand, sequence control and image processing in an image forming apparatus such as a copying machine or a printer is complicated, and the scale of an application specific integrated circuit (ASIC) exclusively therefor is increased. The
- 15 capacity of the exclusive ASIC has become much larger as compared with the capacity of a control chip used for the control of a power source.
- So, even if as shown as an example of related art in Fig. 4 of the accompanying drawings, a power source control chip portion (high voltage control means 2) is added to the "extra portion" of the aforesubscribed large-scale ASIC exclusively for sequence control/image processing, the scale of the entire circuit is not changed greatly, nor the cost
- 20 25 is changed.

The mounted position of the ASIC exclusively for sequence control/image processing, however, is on

the sequence control unit 400 or an image processing unit, and these units are units discrete from a switching power source and a high voltage source unit 300, and the mounted position in the image forming apparatus often becomes separate. So, the sequence control unit 400 or the image processing unit and the switching power source and the high voltage source unit 300 are connected together by bundle lines or the like, and the number of signal lines becomes great. Mentioning the high voltage source of the latest color copying machine as an example, there are necessary high voltage outputs for four colors in each of charging (AC+DC), developing (AC+DC) and transferring, and sheet separation and further, auxiliary charging, auxiliary transferring, cleaning, charge removing, etc. as required, and the number of bundle lines reaches the number of bundle lines corresponding to the drive signal, the detection signal, etc. of the respective outputs. Consequently, it is conceivable to digitize the drive information and detection information of each output and effect time division control by serial communication, but in such case, it becomes necessary to mount an encoder/decoder and digital circuits such as a serial communication circuit and a handshake also in the interior of the high voltage source. Thus, even if the power source control chip portion is added to the

"extra portion" of the ASIC exclusively for sequence control/image processing, the aforementioned digital circuits further exist in the high voltage source portion, and any cost merit cannot be provided.

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SUMMARY OF THE INVENTION

The present invention has been made under such a situation, and provides a power supply apparatus and a switching power supply apparatus in which there
10 is no digital circuit on a power supply side such as output producing means and voltage producing means and the number of signal lines between the power supply side and a control side such as ASIC for sequence control/image processing can be decreased,
15 and an image forming apparatus using these power supply apparatuses.

That is, in the present invention, the power supply apparatus is constructed as described in items (1) and (2) below, the switching power supply apparatus is constructed as described in items (6) to (12) below, and the image forming apparatus is constructed as described in items (3) to (5) and (13) below.

(1) There is provided a power supply apparatus
25 provided with a sequence control unit to which a power source control chip is added, output producing means for producing an output conforming to a PWM

- signal from the power source control chip, and feeding back an output detection signal to the power source control chip, and a signal line for connecting the power source control chip and the output producing means together, wherein a modulated wave amplitude-modulated with the PWM signal as a carrier wave and with the output detection signal as a modulating wave is transmitted to the signal line.
- (2) There is provided a power supply apparatus provided with an image processing unit to which a power source control chip is added, output producing means for producing an output conforming to a PWM signal from the power source control chip, and feeding back an output detection signal to the power source control chip, and a signal line for connecting the power source control chip and the output producing means together, wherein a modulated wave amplitude-modulated with the PWM signal as a carrier wave and with the output detection signal as a modulating wave is transmitted to the signal line.
- (3) The sequence control unit comprises an ASIC and is applied to an image forming apparatus in which sequence control is effected.
- (4) The image processing unit comprises an ASIC and is applied to an image forming apparatus in which image processing is effected.
- (5) The output producing means produces a high

voltage output for at least one of charging,
transferring, developing, sheet separating, cleaning
and charge removing.

- (6) A switching power supply apparatus having
- 5 voltage producing means for producing a predetermined
voltage and controlling means for controlling the
voltage by a digital circuit, the voltage producing
means having a transformer, rectifying and smoothing
means for rectifying and smoothing the output of the
10 transformer, driving means for driving the
transformer, detecting means for detecting an output
obtained by the rectifying and smoothing means, and
modulating means for modulating with a detection
output detected by the detecting means as a
15 modulating wave, the control means having
demodulating means for demodulating the detection
output from a signal modulated by the modulating
means, sample/hold means for sampling/holding the
demodulated detection output, A/D converting means
20 for converting the output of the sample/hold means
into a digital signal, comparing means for comparing
the output of the A/D converting means with an output
target value, a counter operating in conformity with
the output of the comparing means, and PWM signal
25 producing means for producing a PWM signal for
driving the driving means in conformity with the
output of the counter.

(7) The modulating means modulates with a PWM signal for switching the driving means as a carrier wave.

(8) The modulating means amplitude-modulates
5 the carrier wave.

(9) The modulating means varies the signal level of a period for which the PWM signal switches on the driving means to thereby amplitude-modulate.

(10) The modulating means varies the signal
10 level of a period for which the PWM signal switches off the driving means to thereby amplitude-modulate.

(11) The control means is made integral with the ASIC for sequence control as a control chip.

(12) The control means is made integral with
15 the ASIC for image processing as a control chip.

(13) The switching power supply apparatus is a high voltage source for an image forming apparatus.

BRIEF DESCRIPTION OF THE DRAWINGS

20 Fig. 1 is a block diagram schematically showing the construction of an example of the conventional art.

Fig. 2 is a circuit diagram showing the details of high voltage producing means in Fig. 1.

25 Fig. 3 is a block diagram showing the details of high voltage control means in Fig. 1.

Fig. 4 is a block diagram schematically showing

the construction of an example of related art.

Fig. 5 is a block diagram schematically showing the construction of the essential portions of Embodiment 1.

5 Fig. 6 is a circuit diagram showing the details of high voltage producing means in Fig. 5.

Fig. 7 is a block diagram showing the details of high voltage controlling means in Fig. 5.

10 Fig. 8 shows the waveform of a PWM/detection signal.

Fig. 9 is a circuit diagram showing the construction of high voltage producing means in Embodiment 2.

15 Fig. 10 is a block diagram showing the construction of high voltage controlling means in Embodiment 2.

Fig. 11 shows the waveform of a PWM/detection signal.

20 DESCRIPTION OF THE PREFERRED EMBODIMENTS

Some embodiments of the present invention will hereinafter be described in detail with respect to embodiments of an image forming apparatus. Each embodiment is one in which high voltage control means (power supply control chip) is contained in a sequence control unit (ASIC exclusively for sequence control), but can equally be carried out in a form in

which the high voltage controlling means (power source control chip) is contained in an image processing unit (ASIC exclusively for image processing).

5 (Embodiment 1)

An image forming apparatus which is Embodiment 1 will hereinafter be described with reference to Figs. 5 to 8.

Fig. 5 is a diagram showing the construction of 10 a high voltage source apparatus for forming an image forming process in the present image forming apparatus, and Figs. 6 and 7 are diagrams showing high voltage producing means 1A and high voltage controlling means 2A, respectively, in detail. The 15 constructions of charging, transferring, developing, sheet separating, cleaning, charge removing, etc. which are image forming process elements are similar to those of conventional ones and need not be described.

20 In Fig. 5, a high voltage unit 500 has the high voltage producing means 1A. A sequence control unit 600 has the high voltage controlling means 2A for controlling the high voltage producing means 1A, and a controller 3. The essential portion of the 25 sequence control unit 600 is comprised of an ASIC exclusively for sequence control to which a power source control chip which is the high voltage

controlling means 2A is added. The controller 3 is provided by a controller for sequence control. The high voltage producing means 1A is a power source for the image forming process, and a high voltage output 5 is supplied as A, for example, to a transfer roller which is an image forming process element.

Each part will first be described. An inverter transformer T1 boosts and converts a voltage inputted to a primary side in conformity with the turn ratio 10 between the primary side and a secondary side and the ON/OFF time ratio of a PWM signal, and outputs a predetermined voltage to the secondary side. A switching element Q4 drives the inverter transformer T1. An analog buffer 51 is an impedance converter 15 for outputting an output voltage detected by output voltage detecting means 1-3 at a low impedance. Modulating means 1-4 is constituted by a reference voltage Vref, resistors R1, R2 and the analog buffer 51. An analog comparator 52 receives a PWM/detection 20 signal which will be described later at a high impedance, and switches on the switching element Q4 when the voltage level of one terminal side is a predetermined value or greater, and switches off the switching element Q4 when the aforementioned voltage 25 level is less than the predetermined value.

A sample hold circuit 9 latches an input signal for a predetermined period. An A/D converter 10

converts an analog signal into a digital signal. A digital comparator 11 outputs the result of the comparison of the magnitude of two inputs. A PWM circuit 13 prepares a pulse signal (PWM signal) of an 5 ON/OFF time ratio conforming to the value of an up-down counter 12. Gate means 21 is used to demodulate a detection signal from a PWM/detection signal (amplitude modulating wave) which will be described later on the basis of a sampling signal and the PWM 10 signal.

A control circuit 2-5 governs the control of each element, whereby each element in the high voltage controlling means 2A is controlled appropriately.

15 Description will now be made of the connection of each part and element and a series of operations.

(a) High Voltage Producing Means 1A

A predetermined voltage as B-voltage is inputted to the primary side input of the inverter 20 transformer T1, and the other end of the inverter transformer T1 is connected to the switching element Q4. The PWM/detection signal open-collector-outputted from the high voltage controlling means 2A which will be described later, and inputted through a 25 signal line 4 has only its PWM signal component, i.e., pulse width information, taken out by the analog comparator 52 and inputted to the gate of the

switching element Q4. The switching element Q4 performs a switching operation in conformity with the PWM signal to thereby obtain a pulse output conforming to the turn ratio and the PWM signal on 5 the secondary side of the inverter transformer T1. The pulse output is rectified by a diode D301 and a capacitor C201 and produces a DC high voltage output on the cathode side of the diode D301. Boosting and rectifying means 1-2 and driving means 1-1 are 10 generally called fly-back type (or ON/OFF type) converters, and output a higher voltage as the ON time percentage in the switching operation of the switching element Q4 is greater.

The high voltage output is supplied to a high 15 voltage load (e.g. a charger or the like) and contributes to an electrophotographic image forming process.

A resistor R101 and a resistor R102 are connected to the high voltage output, and a voltage 20 dropped by the resistance division of the resistors R101 and R102, i.e., a detection signal of the high voltage output, is outputted. The detected signal is amplitude-modulated by the analog buffer 51, the reference voltage Vref and the resistors R1, R2 with 25 the PWM signal open-collector-outputted from the high voltage controlling means 2A as a carrier wave and with the detection signal as a modulating wave

(modulating signal).

(b) High Voltage Controlling Means 2A

The PWM/detection signal (modulated wave) amplitude-modulated in the high voltage producing means 1A has a waveform as shown in Fig. 8. The PWM/detection signal is an open collector output and is pulled up to an output voltage detection signal level in the high voltage producing means 1A and therefore, by gate means 21, the PWM signal operates the sample hold circuit 9 at high timing on the basis of a sampling signal to thereby demodulate the output voltage detection signal, and input it to the A/D converter 10. The A/D converter 10 converts the high voltage output detection signal into digital data, which is inputted to one of the input terminals of the digital comparator 11.

The controller 3 in the sequence control unit 600 for controlling the high voltage unit 500 outputs digital data corresponding to the output set value (target value) of the high voltage output A. The target value is inputted to the other input terminal of the digital comparator 11.

In the digital comparator 11, the high voltage output detection signal converted into the digital data is compared with the target value given from the controller 3. If the high voltage output detection signal is smaller than the target value, a count

value which is the output of the up-down counter 12 is made greater by a predetermined number, and if the high voltage output detection signal is greater than the target value, the count value of the up-down counter 12 is made smaller by the predetermined number. The PWM circuit 13 produces an appropriate PWM signal in conformity with the count value of the up-down counter 12. A transistor Q22 makes the PWM signal into the PWM/detection signal of the open collector output and outputs it. The PWM/detection signal is a signal which becomes a driving signal for the switching element Q4 in the high voltage producing means 1A, and determines the switching time percentage of the switching element Q4. When the count value of the up-down counter 12 becomes great, the time percentage for which the switching element Q4 is switched on is made great, whereby the high voltage produced by the high voltage producing means 1A is made great. Also, when the count value of the up-down counter 12 becomes small, the time percentage for which the switching element Q4 is switched on is made small, whereby the high voltage produced by the high voltage producing means 1A is made small. By the above-described operation, the high voltage output A is controlled to a predetermined value conforming to the target value.

As described above, according to the present

embodiment, there is no digital circuit on the high voltage unit side, and the number of signal lines between the high voltage unit and the sequence control unit is decreased, and the high voltage 5 source control chip is contained in the ASIC exclusively for sequence control and therefore, a cost merit can be provided.

The high voltage output can be supplied as a high voltage output not only for the illustrated 10 charging and transferring, but also auxiliary charging, auxiliary transferring, cleaning, charge removing etc.

(Embodiment 2)

An image forming apparatus which is Embodiment 15 2 will now be described with reference to Figs. 9 to 11. While Embodiment 1 is characterized in that modulation is effected at the timing whereat the switching element Q4 is switched on, and the voltage detection level is sent to the high voltage 20 controlling means, the present embodiment is characterized in that modulation is effected at the timing whereat the switching element Q4 is switched off, and the voltage detection level is sent to the high voltage controlling means.

25 The construction of the sequence control unit, the construction of the high voltage unit, the connection relation between the sequence control unit

and the high voltage unit, and each part in the present embodiment are substantially similar to those in Embodiment 1 and therefore need not be described, but the description of Embodiment 1 is invoked.

5 Figs. 9 and 10 show the constructions of high voltage producing means 1B and high voltage controlling means 2B, respectively.

10 The high voltage producing means 1B produces a high voltage output A for forming the image forming process, and the high voltage output A is supplied to e.g. a transfer roller which is an image forming process element.

The connection of each part and element and a series of operations will now be described.

15 (a) High Voltage Producing Means 1B

A predetermined voltage as B-voltage is inputted to the primary side input of the inverter transformer T1, and the other end of the inverter transformer T1 is connected to the switching element Q4. A PWM/detection signal outputted from the high voltage controlling means 2B which will be described later has only its PWM signal component, i.e. pulse width information, taken out by the analog comparator 52 and inputted to the gate of the switching element Q4. The switching element Q4 performs the switching operation in conformity with the PWM signal to thereby obtain a pulse output conforming to the turn

ratio and the PWM signal on the secondary side of the inverter transformer T1. The pulse output is rectified by the diode D301 and the capacitor C201, and a DC high voltage output is produced on the 5 cathode side of the diode D301. The boosting and rectifying means 1-2 and the driving means 1-1 are generally called fly-back type (or ON/OFF type) converters, and output a higher voltage as the ON time percentage in the switching operation of the 10 switching element Q4 is greater.

The high voltage output A is supplied to a high voltage load (e.g. a charger or the like) and contributes to the electrophotographic image forming process.

15 The resistor R101 and the resistor R102 are connected to the high voltage output A, and a voltage dropped by the resistance division of the resistors, i.e., the detection signal of the high voltage output is outputted thereto. The detected signal amplitude-
20 modulates a PWM/detection signal outputted from the high voltage controlling means 2B through the analog buffer 51 and the resistor R1. That is, at the timing whereat the PWM/detection signal is open (a transistor Q23 is OFF), the signal assumes an output
25 voltage detection level as shown in Fig. 11, and is transmitted to the high voltage controlling means 2B.

(b) High Voltage Controlling Means

The PWM/detection signal amplitude-modulated in the high voltage producing means 1B has a waveform as shown in Fig. 11. At the timing whereat the PWM/detection signal is open (the transistor Q23 is OFF), this signal assumes the output voltage detection level as shown in Fig. 11 and therefore, on the basis of a sampling signal, the sample hold circuit 9 is operated by the gate means 21 at the timing whereat the PWM signal is open to thereby demodulate the output voltage detection signal, which is then inputted to the A/D converter 10. The A/D converter 10 converts the high voltage output detection signal into digital data, which is inputted to one of the input terminals of the digital comparator 11.

The controller 3 in the sequence control unit for controlling the high voltage unit outputs digital data corresponding to the output set value (target value) of the high voltage output A. The target value is inputted to the other input terminal of the digital comparator 11.

In the digital comparator 11, the high voltage output detection signal converted into the digital data is compared with the target value given from the controller 3. If the high voltage output detection signal is smaller than the target value, the count value of the up-down counter 12 is made greater by a

predetermined number, and if the high voltage output detection signal is greater than the target value, the count value of the up-down counter 12 is made smaller by the predetermined number. The PWM circuit 5 13 produces an appropriate PWM signal in conformity with the count value of the up-down counter 12. The PWM signal is made into the PWM/detection signal of an open collector output by the transistor Q23 and is outputted. The PWM/detection signal is a signal 10 which becomes a drive signal for the switching element Q4 in the high voltage producing means 1B, and determines the switching time percentage of the switching element Q4. When the count value of the up-down counter 12 becomes great, the time percentage 15 for which the switching element Q4 is switched on is made great, whereby the high voltage produced by the high voltage producing means 1B is made great. Also, when the count value of the up-down counter 12 becomes small, the time percentage for which the 20 switching element Q4 is switched on is made small, whereby the high voltage produced by the high voltage producing means 1B is made small. By the above-described operation, the high voltage output A is controlled to a predetermined value conforming to the 25 target value.

As described above, according to the present embodiment, there is no digital circuit on the high

voltage unit side and the number of signal lines between the high voltage unit and the sequence control unit is decreased, and the high voltage source control chip is contained in the ASIC

5 exclusively for sequence control and therefore, a cost merit can be provided.

The high voltage output can be supplied as a high voltage output not only for the illustrated charging and transferring, but also for developing,

10 sheet separating, auxiliary charging, auxiliary transferring, cleaning, charge removing, etc.

According to the present invention, use is made of the PWM/detection signal modulated by the output voltage detection signal with the PWM signal for

15 driving the driving means in the high voltage producing means as a carrier wave, whereby the absence of the digital circuit on the power source side and a decrease in the number of signal lines between the power source side and the control side

20 can be realized.

Thereby, the cost merit by making the ASIC exclusively for sequence control/image processing and the power source control chip portion integral with each other can be provided.

25 While the present invention has been described above with respect to the preferred embodiments thereof, of course, the present invention is not

restricted to these embodiments, but various changes and modifications are possible within the scope of the invention as defined in the appended claims.